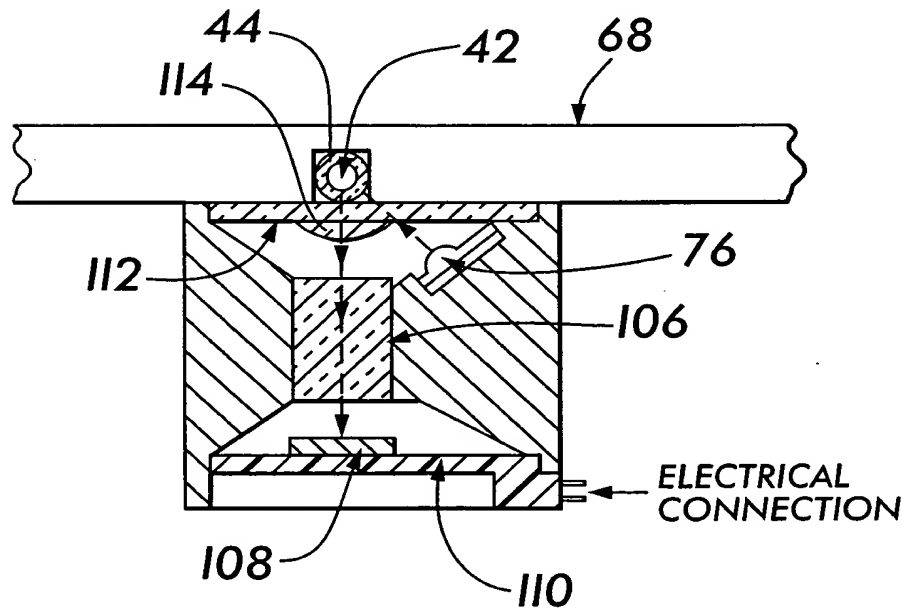
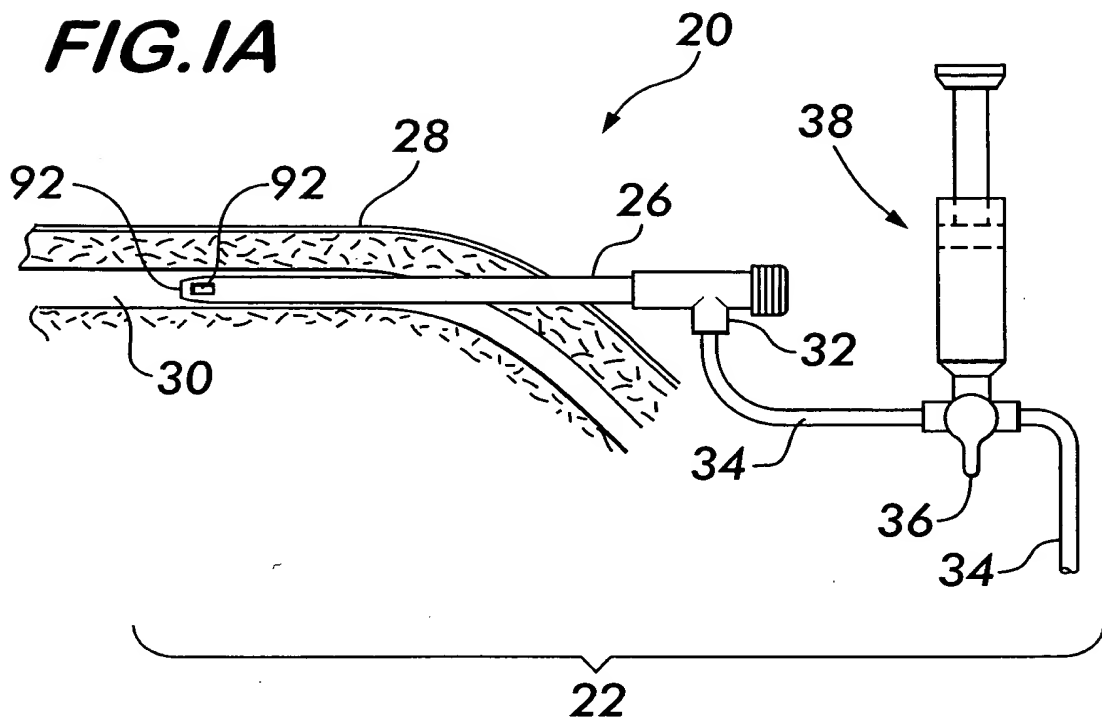
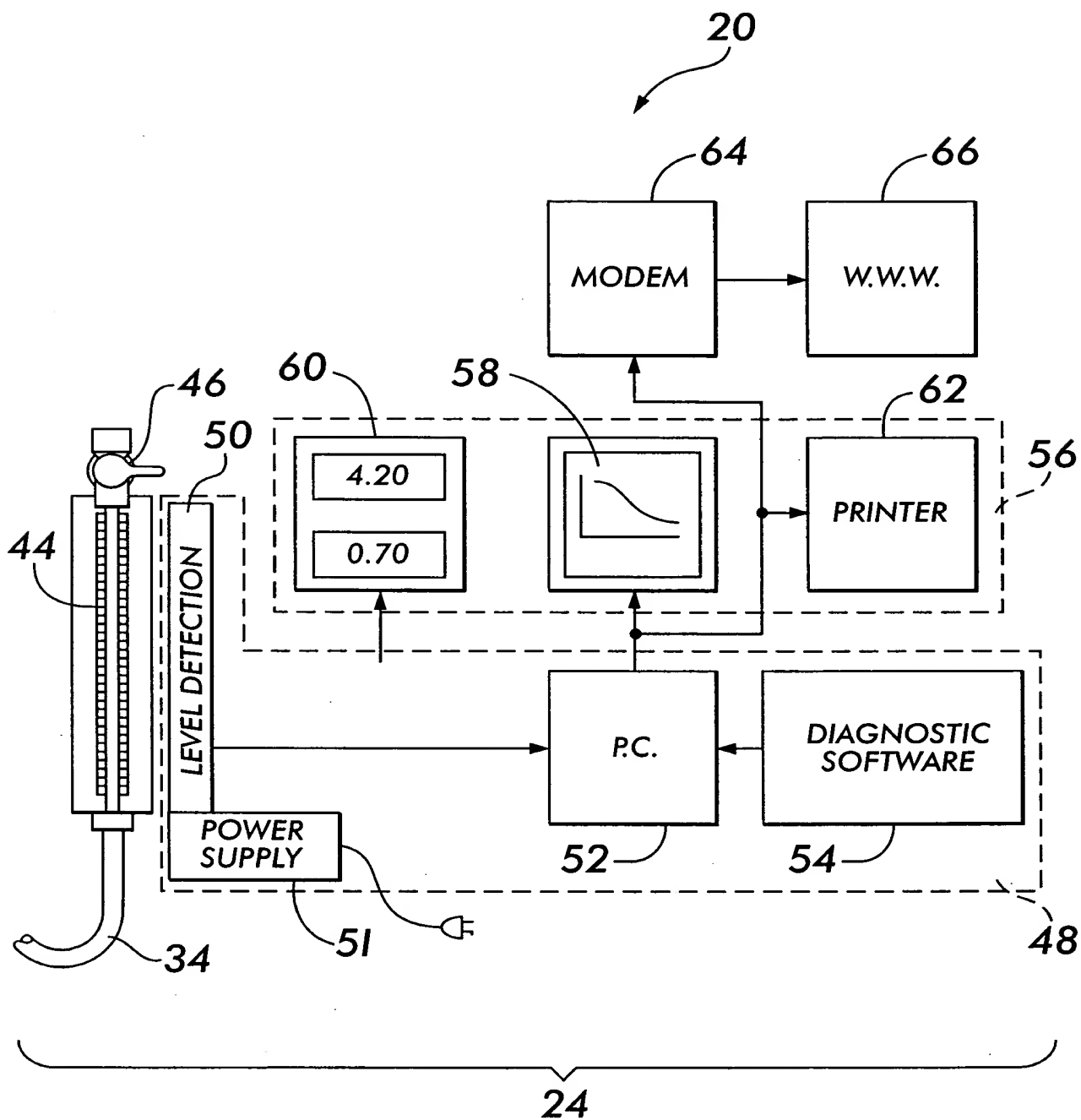
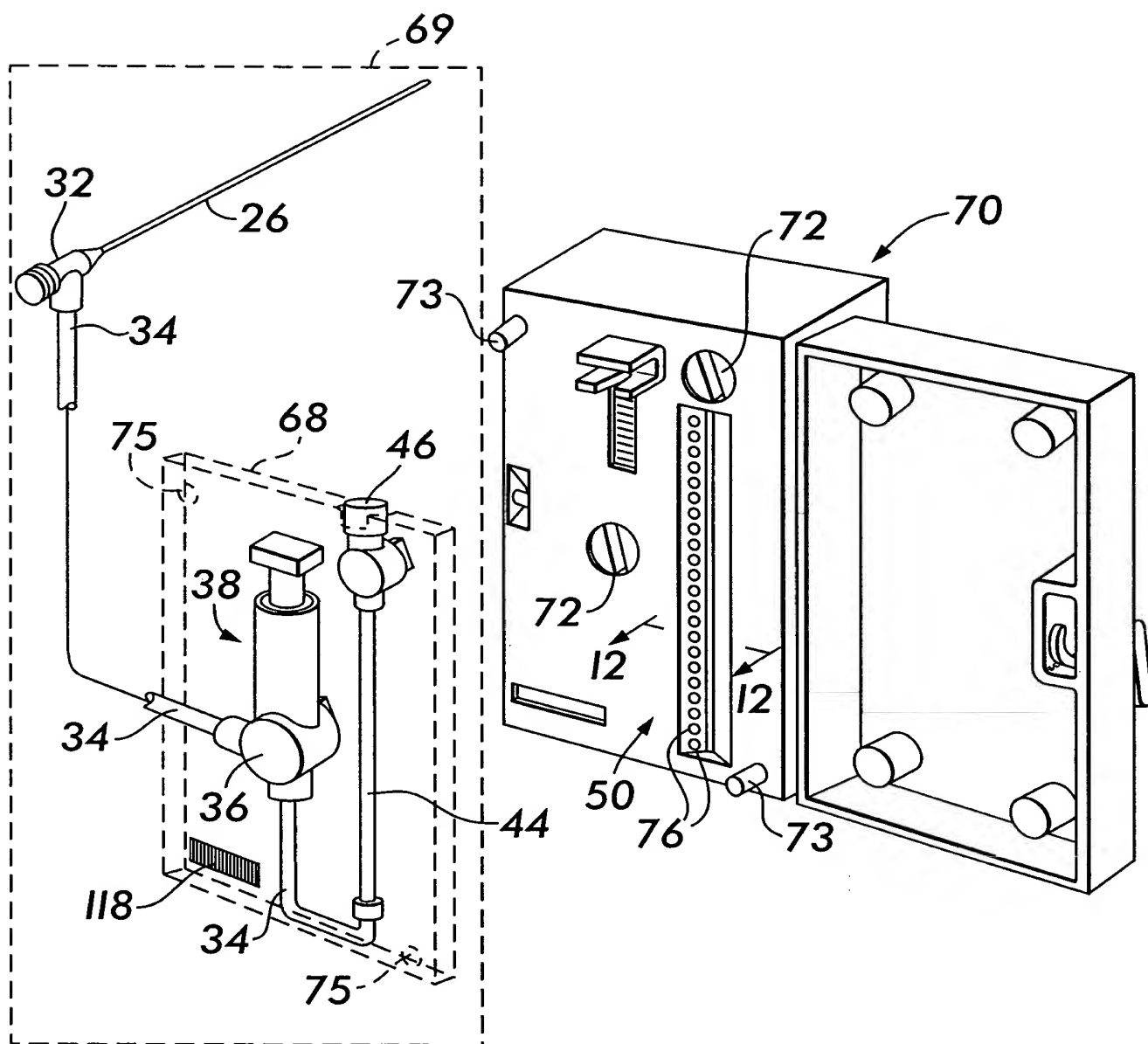


**FIG. 1A****FIG. 12**

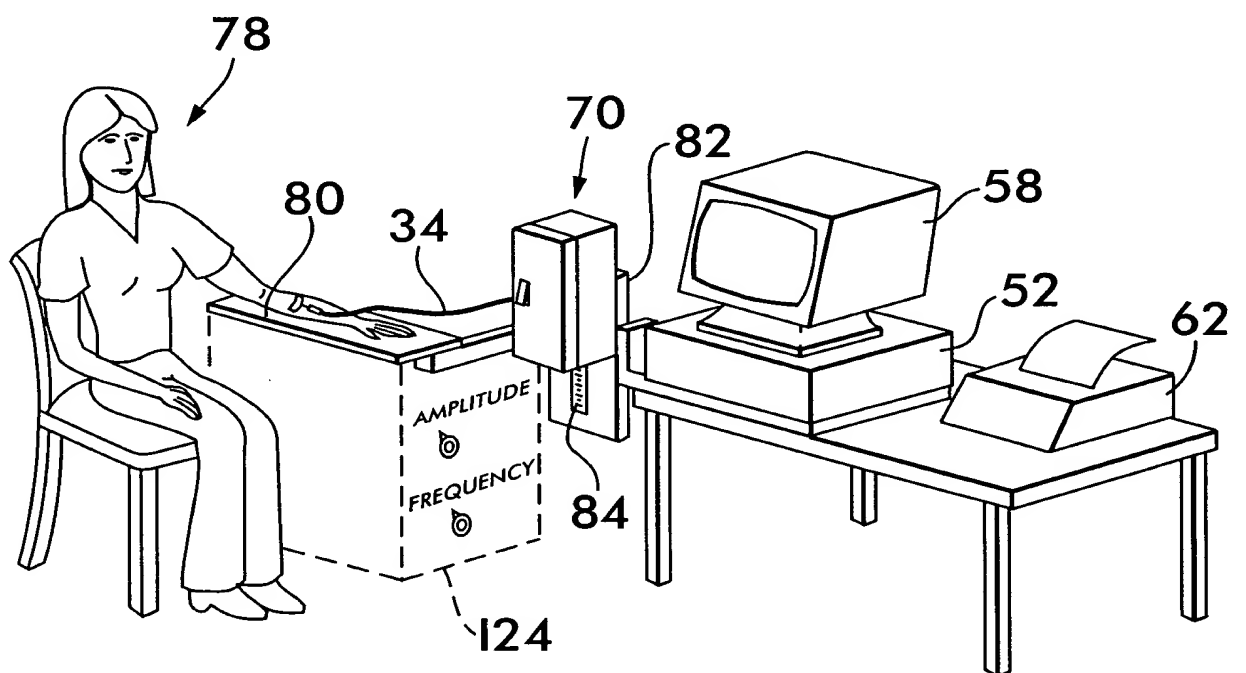


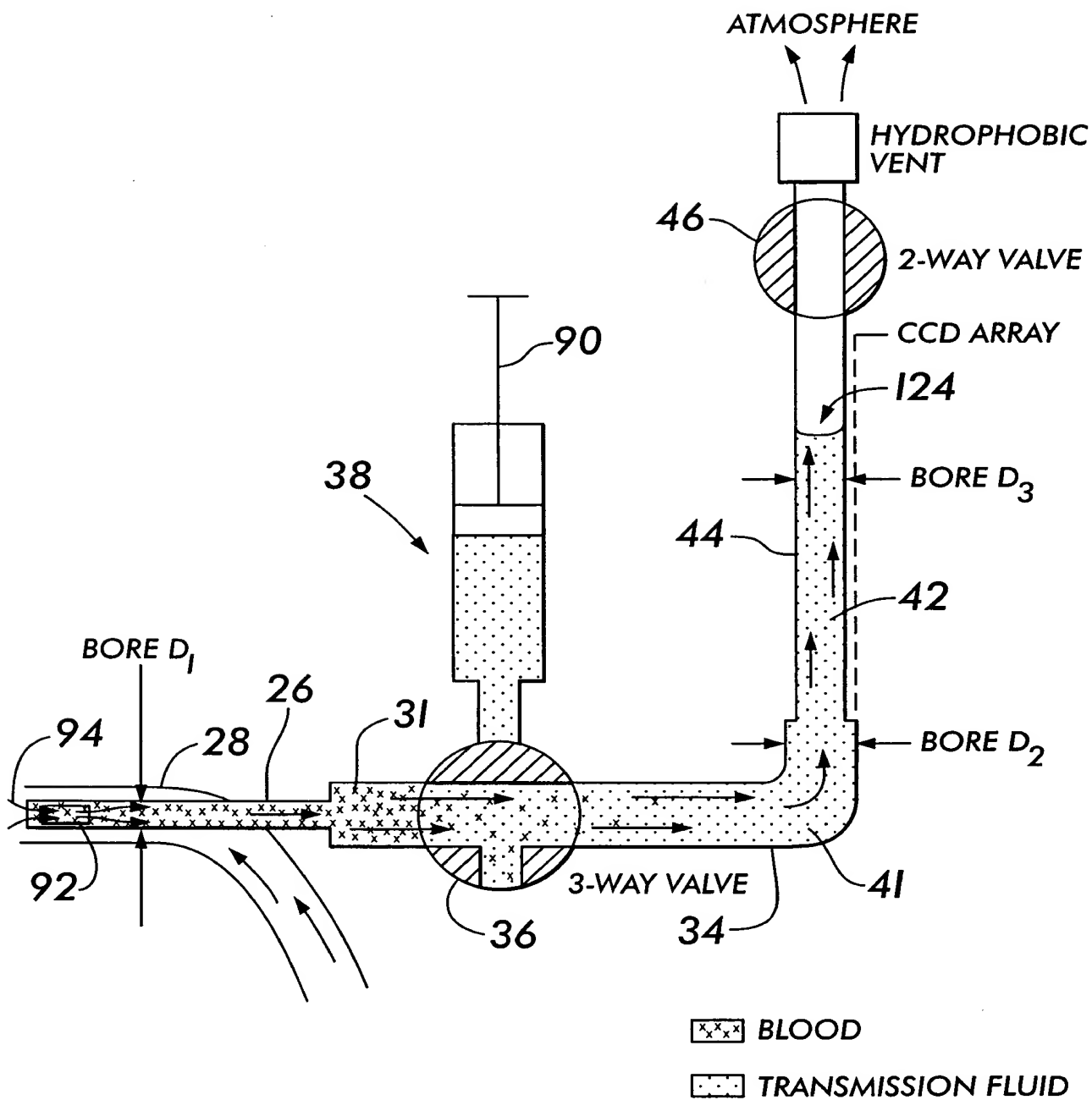
**FIG.1B**



**FIG. 2A**

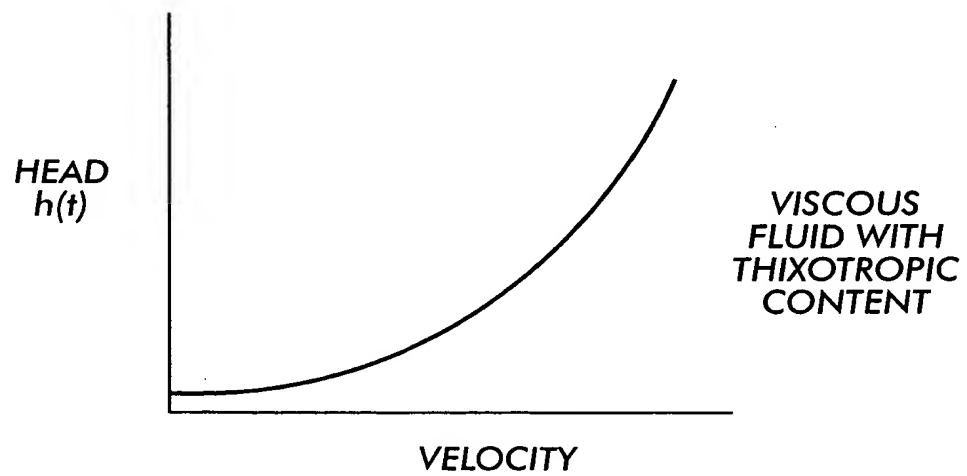
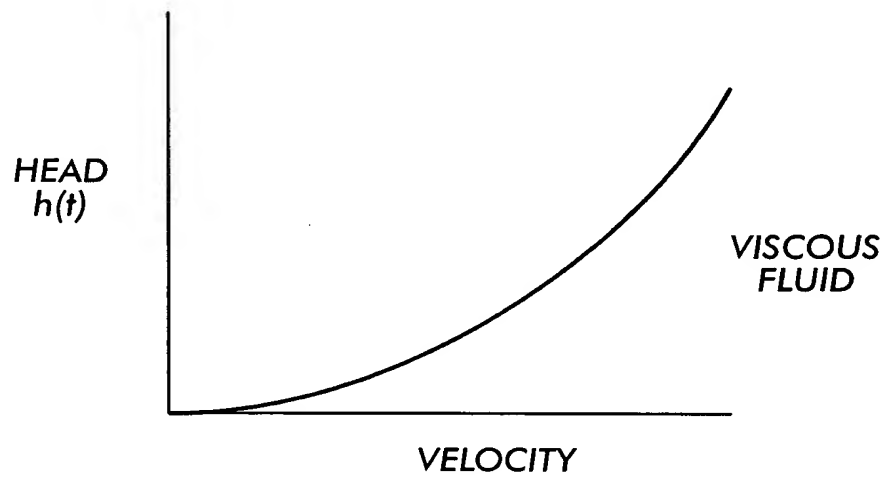
**FIG. 2B**





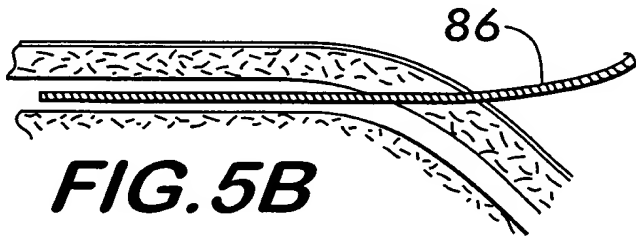
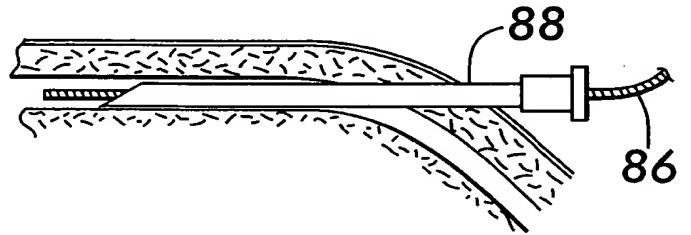
**FIG. 3**

**FIG.4**

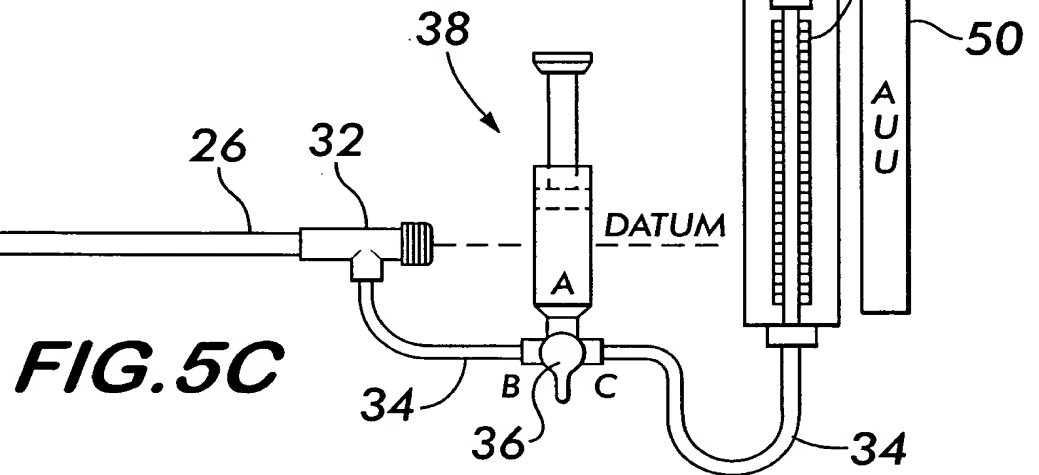


**FIG.14**

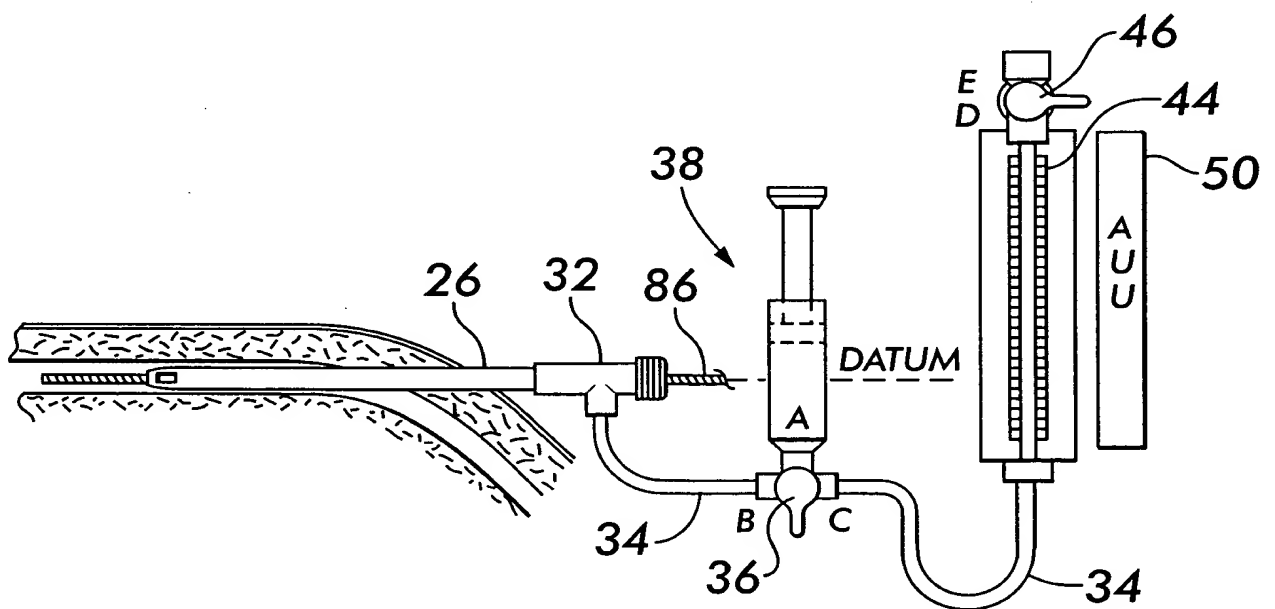
**FIG. 5A**



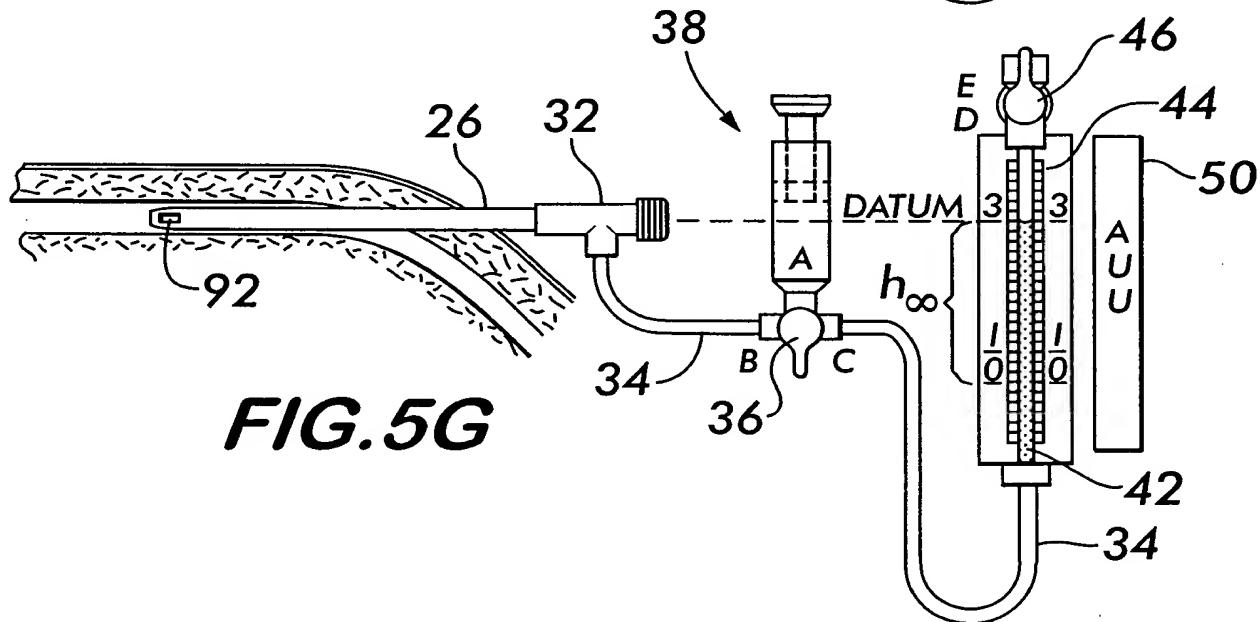
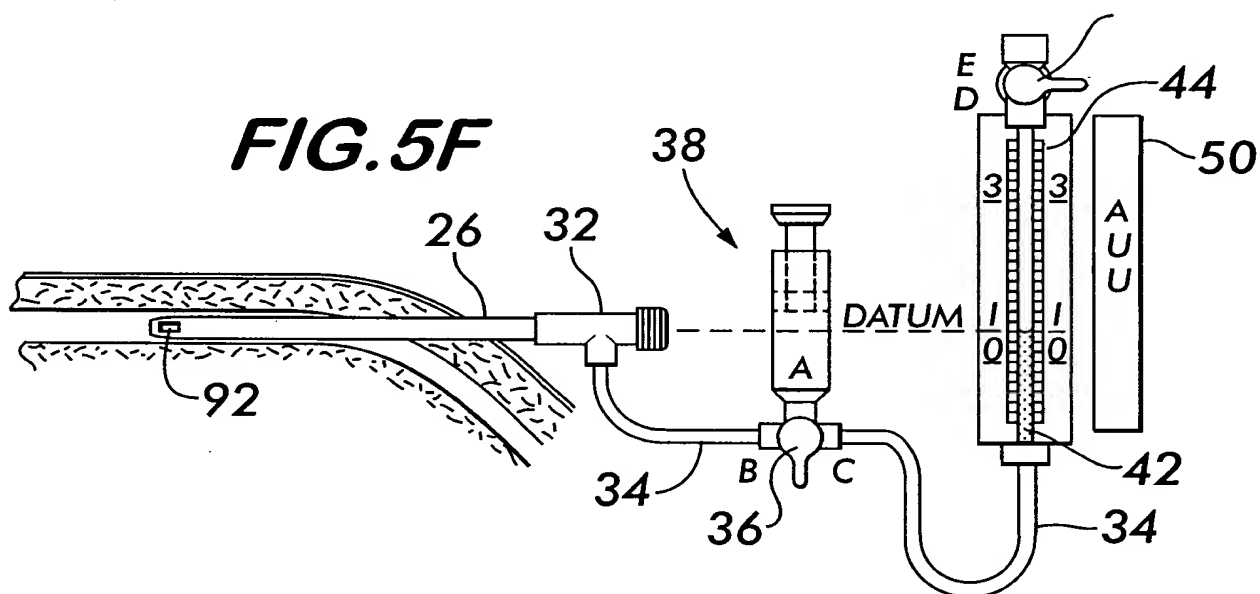
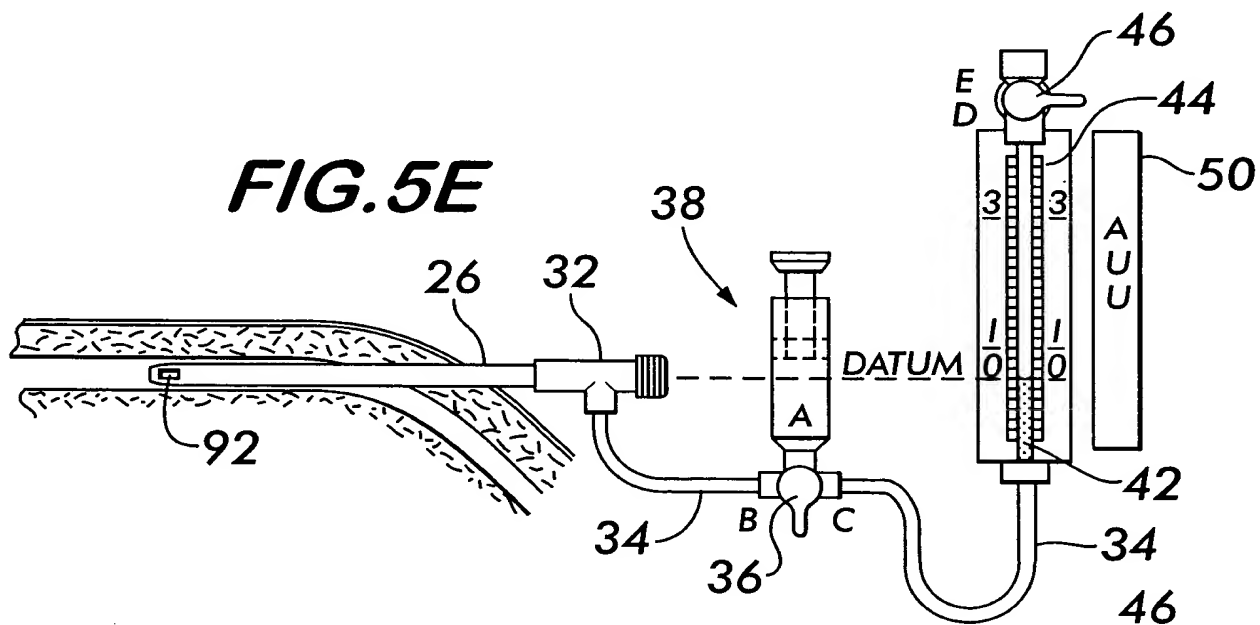
**FIG. 5B**



**FIG. 5C**

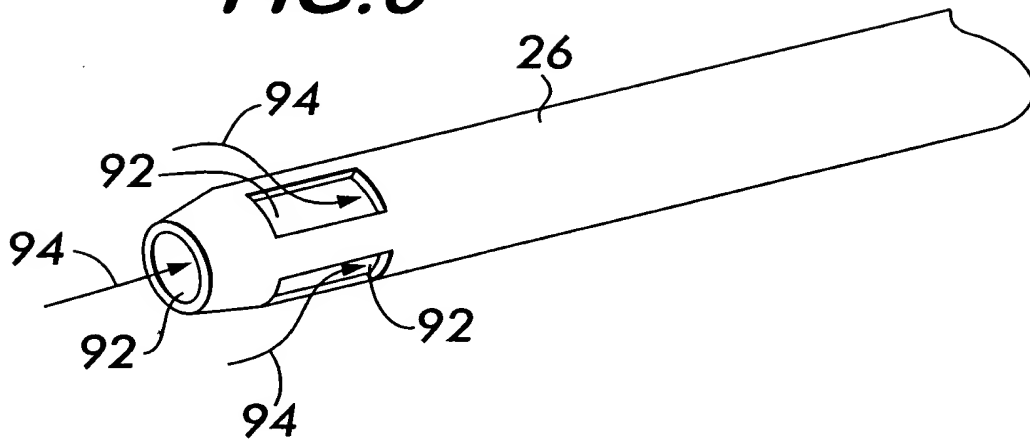


**FIG. 5D**

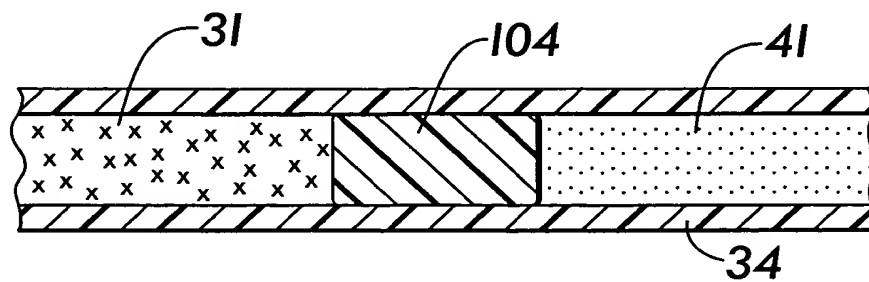
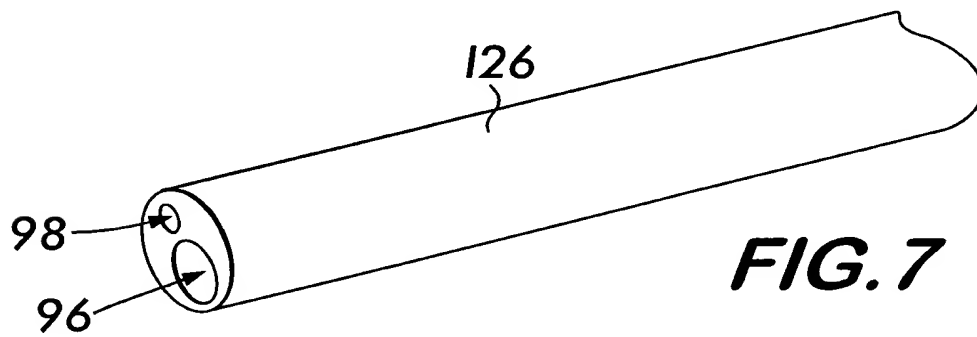




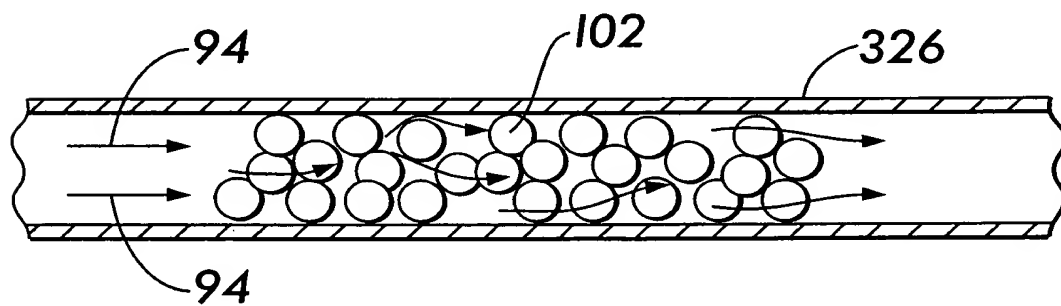
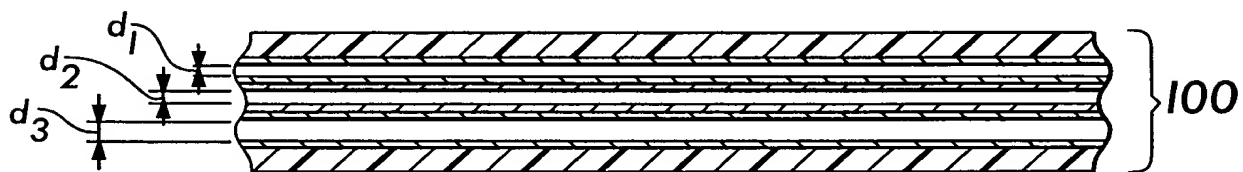
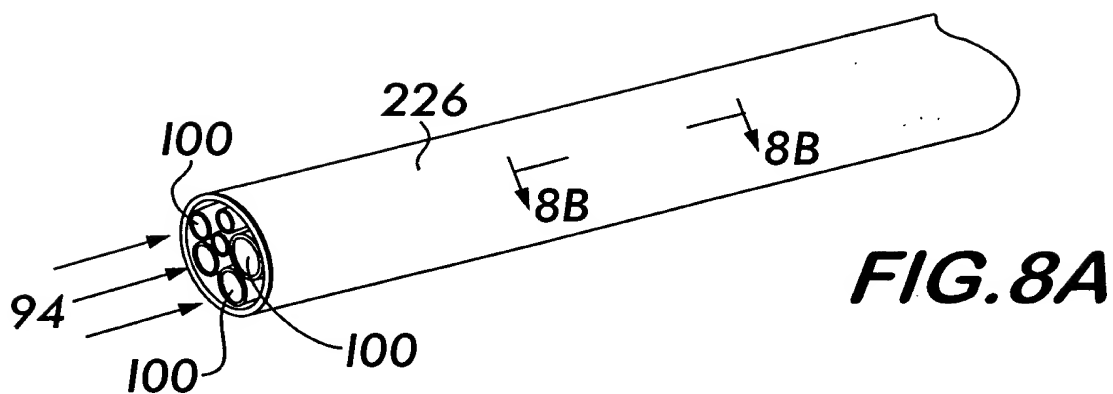
**FIG. 6**

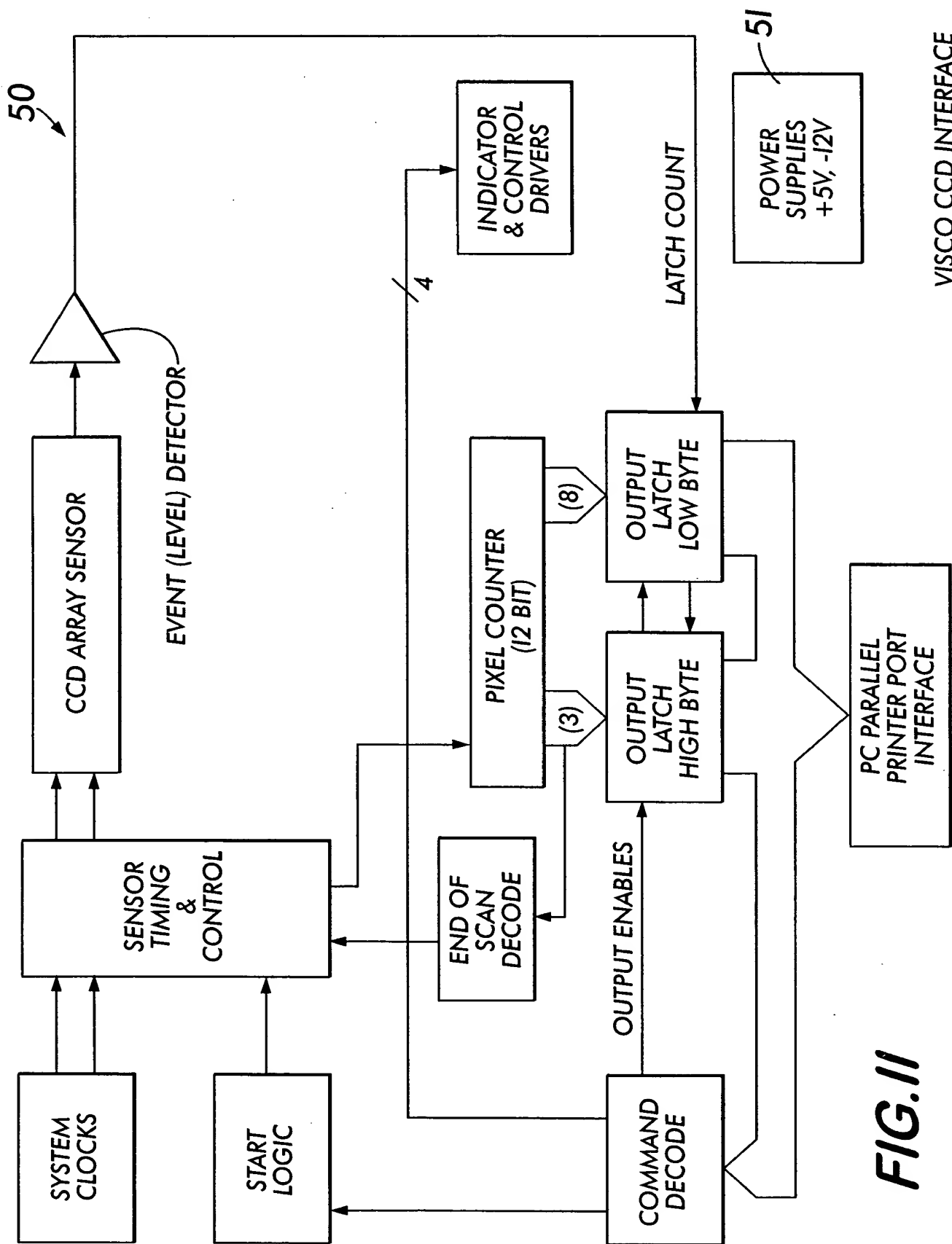


**FIG. 7**



**FIG. 10**





**FIG. 11**

VISCO CCD INTERFACE

FIG. 13

